

10/508948
DT04 Rec'd PCT/PTO 04 OCT 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Georg Rudolf SILLNER

Serial No:

Filed :

For : **PROCESS FOR PROCESSING ELECTRIC COMPONENTS,
ESPECIALLY SEMICONDUCTOR CHIPS, AND
APPARATUS FOR IMPLEMENTING THIS PROCESS**

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to prosecution, please amend the instant
application as follows: